

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended): A noise cancel circuit for removing noise components in an input audio signal, comprising:

an LPF for blocking a sub-signal and a pilot signal in a high frequency band of the input audio signal and passing only a main signal of the input audio signal;

an interpolation circuit for interpolation processing on the main signal output from the LPF,

a noise detection circuit for detecting the noise portion of said input audio signal, ~~and~~

a timer for starting counting in response to an output signal from the noise detection circuit and generating a switching signal during a predetermined period of time while counting is performed; and

a selection circuit for selecting one of output signals from a delay circuit and the interpolation circuit in accordance with the switching signal, thereby serving to replace ~~replacing~~ the noise portion of said input audio signal with an output signal from said interpolation circuit according to ~~an~~ the output signal from said noise detection circuit and outputting a signal subjected to interpolation processing in its noise portion without adding high frequency components removed from the input audio signal.

2. (Original): The noise cancel circuit defined in Claim 1, wherein said interpolation circuit executes spline interpolation.

3. (Previously Presented): The noise cancel circuit defined in Claim 1, wherein:

the noise portion of said input audio signal is interpolated by said interpolation circuit according to an output signal from said noise detection circuit.

4. (Previously Presented): The noise cancel circuit defined in Claim 3, further comprising:

a first delay circuit for delaying said input audio signal;

a selection circuit for selecting either the output signal from said interpolation circuit or the delayed input audio signal from said first delay circuit, wherein

said selection circuit is controlled according to the output signal from said noise detection circuit.

5. (Original): The noise cancel circuit defined in Claim 4, wherein said interpolation circuit performs interpolation processing and outputs an interpolation signal regardless of presence or absence of noise components.

6. (Previously Presented): The noise cancel circuit defined in Claim 5, further comprising:

a second delay circuit for delaying said interpolation signal from said interpolation circuit.

7. (Original): The noise cancel circuit defined in Claim 6, wherein said second delay circuit is disposed in a processing stage prior to said interpolation circuit.

8. (Previously Presented): The noise cancel circuit defined in Claim 6, wherein

a delay time of said first delay circuit is determined based on a sum of an interpolation processing time of said interpolation circuit and a delay time of said second delay circuit.

9. (Original): The noise cancel circuit defined in Claim 8, wherein the delay time of said second delay circuit corresponds to a difference obtained by subtracting the interpolation processing time of said interpolation circuit from a time delay between generation and detection of said pulse noise.

10. (Cancelled).

11. (Previously Presented): The noise cancel circuit defined in Claim 1, wherein said input audio signal is an FM radio signal.

12. (Previously Presented): The noise cancel circuit defined in Claim 1, further comprising:

a timer controlling a timing of changing the noise portion of said input audio signal to the output signal from said interpolation circuit.

13. (Previously Presented): The noise cancel circuit defined in Claim 1, further comprising:

a switch for changing the noise portion of said input audio signal to the output signal from said interpolation circuit according to the output signal from said noise detection circuit.